

[54] **SPEECH SYNTHESIZED TIME ANNOUNCEMENT CIRCUIT**

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[58] **Field of Search** 179/89, 18 B, 2 A;
368/13, 63; 381/36

[56] **References Cited**

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[57] **ABSTRACT**

A speech synthesized time announcement circuit for use with a telephone line control circuit. Upon detection of a predetermined number of ringing signal bursts or a predetermined keypad provided data pattern or a predetermined time period, a control microprocessor determines the current time of day and forms a time-of-day message by retrieving selected digitized words from a read-only memory. This message is transferred to a speech processor which converts it to analog format. If the time of day request was received over a telephone line, the control microprocessor causes the speech processor to answer the line and couple the time-of-day message onto the line. If the request was received by internal interrupt or via the keypad, the control processor causes the speech microprocessor to announce the message locally by applying it to a local speaker.

12 Claims, 6 Drawing Figures

